Key Features:
- 50 MHz Test Rate (100 MHz HSCLK)
- 256 I/O channels
- 8–16 M Pattern Memory
- Flexible Configuration (Interchangeable I/O, UVI, ADDA, and LCD)
- Parallel Testing for 32 devices
- Real Parallel Trim/Match function
- Test program/pattern converter (V7, TRI6020, V50, SC312, J750, ITS9K, TS670, ND1)
- Analog PE card option (16 bit–24 bit)
- SCAN test option (512M)
- ALPG test option for Memory
- STDF tools support
- User friendly Windows XP environment
- CRAFT C/C++ programming language
**The Most Flexible Configuration for All Types of Devices**

**The Full Functions - Logic, LCD, LED, ADDA, Power, ALPG, SCAN, Match... etc.**

![Diagram showing the configuration of 3360P](image)

**Specifications**

<table>
<thead>
<tr>
<th>Model</th>
<th>3360P(I/O)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Rate</td>
<td>50MHz</td>
</tr>
<tr>
<td>Pin Channels</td>
<td>Max. 256 Pins</td>
</tr>
<tr>
<td>Pattern Memory</td>
<td>8M (16 M option)</td>
</tr>
<tr>
<td>Parallel Testing Capability</td>
<td>Max. 32 DUTs</td>
</tr>
<tr>
<td>EPA</td>
<td>± 625ps</td>
</tr>
<tr>
<td>Resource Per Pin Architecture</td>
<td>Yes</td>
</tr>
<tr>
<td>DPS (± 10V, ± 2 A)</td>
<td>8 (option: 24→add UVI*1)</td>
</tr>
<tr>
<td>PMU (± 45V, ± 100mA)</td>
<td>16</td>
</tr>
<tr>
<td>PPMU (± 0.5V → 6.5V, ± 35mA)</td>
<td>Per Pin</td>
</tr>
<tr>
<td>Programmable Load (Active)</td>
<td>Per Pin (± 35 mA)</td>
</tr>
<tr>
<td>Windows Environment</td>
<td>Windows XP</td>
</tr>
<tr>
<td>Programming Language</td>
<td>C/C++</td>
</tr>
</tbody>
</table>

**Test Option**

- Hi-V (LCD) Channel: Max. 224 LCD pins
- AD / DA Converter Test Option: 4AWG / 4 DGT (16 Bits AWI board)
- High Accuracy AD/DA: 2 AWG / 2DGT (24bits)
- UVI (DPS ± 10V, ± 500 mA): 16
- SCAN Option: 512M per board
- ALPG Memory Test Option: 16X, 16Y, 16D

**System and Dimension**

- Power consumption: Max. 3KVA
- Only Test Head: W640 x D470 x H639 mm (Max. 90 Kg)

*The “Cable-Mount” is the standard, The “Direct-Mount” solution is option.  
*The specifications are subject to change without notice.

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